

# FIRST ANNOUNCEMENT AND CALL FOR PAPERS

## 2025 SYMPOSIUM ON VLSI TECHNOLOGY AND CIRCUITS

~ Cultivating the VLSI Garden: From Seeds of Innovation to Thriving Growths ~

Rihga Royal Hotel Kyoto, Japan  
Sunday–Thursday, June 8–12, 2025

June 8 Workshops  
June 9 Short Courses  
June 10–12 Technical Sessions



**Paper Submission Deadline: 23:59 JST Monday, January 27, 2025**  
**Details: [www.vlssymposium.org](http://www.vlssymposium.org)**

### Symposium Scope

The Symposium calls for papers in the following areas:

- **Advanced CMOS Platforms, Interconnect and Backside Power Delivery Network (BSPDN) Technologies**
- **Advanced packaging, Chiplet and Heterogeneous Integration Technologies Including 2.5D and 3D**
- **Analog and Mixed-Signal Circuits**
- **Beyond CMOS Devices and Circuits That Utilize New Physics, Including Spin, Photon, and Quantum Dynamics**
- **Biomedical Devices, Circuits, and Systems**
- **Data converters**
- **Device physics, Characterization, Modeling and Reliability**
- **Devices and Accelerators for ML/DL and New Computing, Including Optical and Quantum Computing**
- **Digital Circuits, Hardware Security, Signal Integrity, IOs**
- **DTCO and Design Enablement**
- **Frequency Generation and Clocking Circuits**
- **Memory Technologies, Devices, Circuits, and Architectures**
- **Power Management Devices and Circuits**
- **Processes and Materials for CMOS Scaling and New Devices**
- **Processors and SoCs**
- **Sensors, Imagers, IoT, MEMS, Display Circuits**
- **Wireless / RF Devices, Circuits and Systems**
- **Wireline and Optical Transceivers, Optical Interconnects and Processors**

### Contacts

(Asia and Japan) JTB Communication Design  
[vlssymp@jtbcom.co.jp](mailto:vlssymp@jtbcom.co.jp)

(North America and Europe) Canfield Event Management  
[vlss@vlssymposium.org](mailto:vlss@vlssymposium.org), +1-972-521-9902

### Focus Sessions

In addition to the topics mentioned, the Symposium will highlight Focus Sessions on special topics.

### Short Courses

Full-day short courses will offer in-depth lectures on VLSI-related topics given by distinguished experts from industry and academia.

### Demo Session

A demo session will provide authors of selected papers to showcase posters/videos and demonstrate their work. The best demo paper will be awarded.

### Best Student Paper Award

Selection will be based on quality of the paper and presentation at the Symposium. The winning student will be presented with a certificate and monetary award at the 2026 VLSI Symposium opening session. **At time of submission, the student must be enrolled as a full-time student, be the leading author and presenter of the paper, and indicate consideration for the award.**

### Call for Workshops

The Symposium provides valuable opportunities for volunteers to apply to organize and host short workshops at the Symposium. The call for workshop proposals and details will be announced soon.

### Paper Submission (New Format)

Prospective authors must submit paper abstracts to the Symposium website [www.vlssymposium.org](http://www.vlssymposium.org). **The new three-page Paper format is shown here.** Accepted papers will be published as-submitted with **no revisions permitted**. Authors must follow detailed instructions provided in the “Authors” section of the website, including the Authors’ Guide and Pre-publication Policy. **Extended versions of outstanding papers will be invited for publication in the IEEE Transaction on Electron Devices, IEEE Journal of Solid-State Circuits, and IEEE Solid-State Circuits Letters.**



**Symposium Chairs:**  
Takaaki Tsunomura, Tokyo Electron Limited.  
Mototsugu Hamada, The University of Tokyo

**Symposium Co-Chairs:**  
Vijay Narayanan, IBM T.J. Watson Research Center  
Ron Kapusta, Analog Devices

**Program Chairs:**  
Kazuhiro Endo, Tohoku University  
Sugako Otani, Renesas Electronics Corporation

**Program Co-Chairs:**  
Benjamin Colombeau, Applied Materials  
John Wu, Advanced Micro Devices



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