



*FOR IMMEDIATE RELEASE – 20 MARCH, 2018*

*Technology, Circuits Symposia programs have been aligned for an overlapping schedule from 18 – 22 June at the Hilton Hawaiian Village in Honolulu, HI...*

## **“Technology, Circuits & Systems for Smart Living” Theme for 2018 Symposia on VLSI Technology & Circuits**

HONOLULU, HI (MARCH 20, 2018) – Bringing together a technical program that encompasses ‘big integration’ of a number of critical industry trends – machine learning, IoT, artificial intelligence, wearable/implantable biomedical applications, big data, and cloud computing – the 2018 Symposia on VLSI Technology & Circuits will showcase a convergence of technologies needed for ‘smart living.’ As the microelectronics industry’s premiere international conference covering technology, circuits, and systems, the Symposia continues to define the evolution of innovations that will shape the future of our increasingly connected world.

The Symposia theme of **“Technology, Circuits & Systems for Smart Living”** connects the related plenary presentations, panel discussions, focus sessions, short courses, along with a new Friday Forum on machine learning to provide a unique synergy between advanced technology developments, innovative circuit design, and the applications that they enable – as part of our global society’s transition to a new frontier of smart, connected devices and systems that change the way humans interact with technology – and with each other.

“This year’s Technology program is focused on the critical building blocks needed to realize a truly integrated IoT,” said Mukesh Khare, Symposium on VLSI Technology general chair. “Advanced memory technologies for AI and machine learning, the next wave of advanced computing (supercomputing/cloud/neuromorphic), the cutting edge of CMOS scaling (beyond 5nm/nanowire devices), and the advanced low-power sensors needed to connect them all are just some of the highlights of the Technology program.”

“The Circuits program will examine how the next wave of computing systems need to be designed to realize the potential of AI, machine learning, SOC technology, wearable/implantable biomedical systems, and the IoT,” explained Gunther Lehmann, Symposium on VLSI Circuits general chair. “A demonstration session that showcases real-life applications is designed to enable conference participants to see these innovations first hand.”

The Symposia will also include a series of joint focus sessions that include invited and contributed papers on topics of mutual interest to both technology and circuit attendees. As part of the unique Symposia program, these joint Technology & Circuits focus sessions enable participants to engage in meaningful interaction with their colleagues in different disciplines. In

addition, there will be a joint evening panel session by leading industry experts to address critical issues surrounding major industry developments.

Capping off the joint Symposia program will be a series of nine presentations comprising the Friday Forum on machine learning, a subject area that continues to evolve as an impactful driver of the integrated systems that are part of the Symposia's "Smart Living" theme.

The annual Symposium on VLSI Technology & Circuits will be held at the Hilton Hawaiian Village in Honolulu, Hawaii from June 19-21, 2018, with Short Courses held on June 18 and a special Friday Forum dedicated to machine learning/AI topics on June 22. The two conferences have been held together since 1987, providing an opportunity for the world's top device technologists, circuit and system designers to exchange leading edge research on microelectronics technology, with alternating venues between Hawaii and Japan. A single registration enables participants to attend both Symposia.

### **Sponsoring Organizations**

The Symposium on VLSI Technology is sponsored by the IEEE Electron Devices Society and the Japan Society of Applied Physics, in cooperation with the IEEE Solid State Circuits Society.

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### **Further Information and Registration**

Visit: <http://www.vlssymposium.org>.

### **Media Contacts**

Chris Burke | BtB Marketing Communications  
[chris.burke@btbmarketing.com](mailto:chris.burke@btbmarketing.com) | +1-919-872-8172

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