

Privacy Notice (GDPR version)

Privacy Notice

The 2021 Symposia on VLSI Technology and Circuits (the Symposia hereafter) is the international symposia which will be held on June 13 - 19, 2021 in Japan. We, (JSAP Executive Committee), the organizer of the 2021 Symposia on VLSI Technology and Circuits issues this Privacy Notice to inform you of our collection, access, use and process your personal data in the way described below.

1. Categories of personal data

We hereby inform you that we collect, use and process the following categories of your personal data:

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- Email Address
- IEEE or JSAP Member Number
- Cookie Information

2. Legal basis and purpose (As per Article 6(1)(f) GDPR, we hereby inform you that we use your personal data for the purpose of providing you following services.)

- Registration of participation in the symposia
- Submitting paper to the symposia
- Operational tasks
- Contacting participants in case of emergency
- Preparation of reports
- Conducting questionnaire surveys
- Providing the information of other related conferences
- Providing the list of participants to the organizer of Silicon Nanoelectronics Workshop and Spintronics Workshop

In addition, your personal data may be converted into statistical or aggregated data which cannot be used to identify you and then used to produce statistical research and reports.

For purposes other than those stated above, the Symposia will not use your personal information.

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5. Prior consent

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6. Your rights

As an applicant you have number of rights among them: the right to request access to your personal data, to have it corrected or deleted, and the right to object to our processing of your personal data.

For further information on these rights please refer to our Privacy Policy:

Online link to Privacy Policy: <https://www.jsap.or.jp/english/privacypolicy>

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If you require assistance or further information about how your personal data is used by us then contact to the following company, which will act as the secretariat under a confidentiality agreement.

Secretariat of VLSI Symposia

JTB Communication Design Inc.

Celestine Shiba Mitsui Bldg. 3-23-1, Shiba, Minato-ku, Tokyo 1-5-8335, Japan

E-mail: vlsisymp@jtbcom.co.jp

If you are not completely satisfied with how personal data request was handled, then you may contact your local Data Protection Supervisory Authority.

Administrators of personal information collected for the Symposia

Prof. Tadahiro Kuroda

JSAP Executive Committee Chair of the 2021 Symposia on VLSI Technology and Circuits