

# 2018 SYMPOSIUM ON VLSI CIRCUITS

Hilton Hawaiian Village, HONOLULU, Hawaii, USA  
 Monday – Friday, June 18-22, 2018  
 (June 18 Short Course,  
 June 19-21 Technical Sessions,  
 June 22, Forum)

**Paper Submission Deadline: 23:59 PST Monday, January 29, 2018**  
 For complete conference, author, and registration information, visit:  
<http://www.vlsisymposium.org>

## Scope

The Circuits Symposium is placing special emphasis on several Innovative System focus areas, and encourages paper submissions on:

- Machine and deep learning
- Internet of Things
- Industrial electronics
- Big Data management and analytics
- Robotics and autonomous transportation

In addition, submissions are welcomed in all of the following circuits areas:

- Processors, architectures and SoCs
- Digital circuits, signal integrity, and IOs
- Memory circuits, architectures, and interfaces
- Biomedical circuits
- Sensors, imagers, and display circuits
- Power conversion circuits
- Analog, amplifier and filter circuits
- Wireless receivers and transmitters
- Data converters
- Frequency generation and clock circuits
- Wireline receivers and transmitters



**Symposium Chair**  
 Gunther Lehmann  
 Infineon Technologies  
[gunther.lehmann@infineon.com](mailto:gunther.lehmann@infineon.com)

**Symposium Co-Chair**  
 Makoto Ikeda  
 University of Tokyo  
[ikeda@silicon.u-tokyo.ac.jp](mailto:ikeda@silicon.u-tokyo.ac.jp)

**Program Chair**  
 Ken Chang  
 Xilinx Inc  
[kchang@xilinx.com](mailto:kchang@xilinx.com)

**Program Co-Chair**  
 Ken Takeuchi  
 Chuo University  
[takeuchi@takeuchi-lab.org](mailto:takeuchi@takeuchi-lab.org)

**Secretariat (North America and Europe)**  
 Widerkehr & Associates, Montgomery Village, MD, USA  
 +1-301-527-0900 ext.2, [vlsi@vlsisymposium.org](mailto:vlsi@vlsisymposium.org)

**Secretariat (Japan and Far East)**  
 c/o JTB Communication Design, Inc., Tokyo, Japan  
 +81-3-5657-0777, [vlsisvmp@jtbcom.co.jp](mailto:vlsisvmp@jtbcom.co.jp)



## Highlights

There will be a full four-day overlap between the co-located Symposia on VLSI Technology and Circuits, providing unique opportunities for interaction in the “More than Moore” era.

- Access to both Symposia with a single registration
- Joint Focus Sessions in the following areas:
  - Embedded memory technology
  - Design in scaled technologies
  - 3D and heterogeneous integration
  - Design enablement

The Symposia also offer ample opportunities to network with colleagues in both technical areas

- Two evening panels
- Evening reception and joint banquet
- SSCS Women in Circuits luncheon
- SSCS / EDS Young Professionals event

In addition to technical sessions, attendees participate in interactive events with authors, instructors, and experts from industry and academia

- Multiple short courses on a variety of relevant topics
- Demo session for outstanding papers
- Adjacent Forum on Deep Learning and Artificial Intelligence, Friday, June 22<sup>nd</sup>

## Further Information

The Symposia website is the central resource for additional information, including details on paper submissions and Best Student Paper Award eligibility.

<http://vlsisymposium.org>