

THIRD ANNOUNCEMENT AND CALL FOR PAPERS

2021 SYMPOSIUM ON VLSI CIRCUITS

~VLSI Systems for Lifestyle Transformation~

Kyoto, Japan
Sunday – Saturday, June 13-19, 2021 JST
Workshops
Short Courses
Technical Sessions
Forum



VLSI 2021 will be held in a fully virtual format due to COVID-19.

Paper Submission Deadline: 23:59 JST Monday, February 8, 2021
For detail, please visit: www.vlsisymposium.org

Symposium Scope

The Symposium on VLSI Circuits calls for papers in the following areas:

- Processors, SoCs, quantum computing, and machine learning
- Digital circuits, signal integrity, and IOs
- Hardware security
- Memory circuits, architectures, and interfaces
- Biomedical circuits and systems
- Sensors, imagers, and display circuits
- Power conversion circuits
- Analog building blocks
- Wireless receivers and transmitters
- Data converters
- Frequency generation and clock circuits
- Wireline and optical transceivers

Innovative System Directions

Authors are encouraged to submit papers on “big integration” that showcase innovations that extend beyond a single IC and into the system level. These include focus areas of **Machine and deep learning, FPGA-based accelerators, Internet of Things, Industrial electronics, Big Data management, Biomedical applications, and Robotics and autonomous transportation.**

Focus Sessions

The Symposium will highlight the following Focus Sessions:

- Advanced packaging and 3D integration (Joint)
- Photonics interconnect and compute (Joint)

Short Courses

Three full-day short courses will offer in-depth lectures on the following topics:

- Advanced sensor circuits and systems for IoT (Circuit)
- Computing paradigm shift including memory and new devices (Joint)
- Advanced process and device technology toward 2nm-CMOS and emerging memory (Technology)

Secretariat for VLSI Symposia

(Asia and Japan) c/o JTB Communication Design, Inc., Tokyo, Japan
vlsisymp@jtbcom.co.jp

(America and Europe) IEEE Meetings, Conferences & Events (MCE), Piscataway, NJ USA vlsisymposium@ieee.org

Evening Panel Discussions

All attendees are invited to join evening panel on:

- 3D/Heterogeneous integration including democratization
- New Generation Chip Makers vs. the Incumbents?
- Science, Technology and Educations for New Normal Society

Symposia Demo Session

All demo will be video-based and open to a wide range of authors. The demo session will provide the opportunity to see pre-recorded demonstrations and prototypes of research projects, corresponding to all relevant topics of the Conference. The most attractive and innovative demo will receive the award.

Forum

World-experts will share their perspectives on “Technologies for Post COVID-19 era”, including Communication, Remote / Robotics, 5G, IoT, Sensors and Security.

Workshops

Enthusiastic organizers will provide the additional learning opportunities of VLSI related topics for participants. The topics of the workshops will be announced at the end of January.

Best Student Paper Award

Selection will be based on quality and presentation of the paper at the Symposium. The winning student will be presented an award at the 2022 Symposium opening session. **The student must be enrolled as a full-time student at the time of submission, be the leading author and presenter of the paper, and apply for the award when submitting the paper.**

Paper Submission Prospective authors must submit two-page papers and abstracts using the Symposia’s website, www.vlsisymposium.org. Accepted papers will be published as submitted, with **no revisions permitted**. Authors must follow detailed instructions provided within the “Authors” section of the website, including the Authors’ Guide and Pre-publication Policy. All the speakers can join on-line if they wish. Extended versions of selected papers from the Symposium will be published in a **Special Issue of the IEEE Journal of Solid-State Circuits and IEEE Solid-State Circuits Letters**.



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