

FIRST ANNOUNCEMENT AND CALL FOR PAPERS

## 2021 SYMPOSIUM ON VLSI CIRCUITS

~VLSI Systems for Lifestyle Transformation~

RIHGA Royal Hotel Kyoto, Japan  
Monday – Thursday, June 14-17, 2021  
Workshops  
Short Courses  
Technical Sessions  
Forum

Depending on the situation of COVID-19,  
virtual platform will be prepared.



Paper Submission Deadline: 23:59 JST Monday, February 8, 2021

For detail, please visit: [www.vlssymposium.org](http://www.vlssymposium.org)

### Symposium Scope

The VLSI Circuit symposium calls for papers in the following areas:

- Processors, SoCs, and machine learning
- Digital circuits, signal integrity, and IOs
- Hardware security
- Memory circuits, architectures, and interfaces
- Biomedical circuits
- Sensors, imagers, and display circuits
- Power conversion circuits
- Analog amplifier, and filter circuits
- Wireless receivers and transmitters
- Data converters
- Frequency generation and clock circuits
- Wireline and optical transceivers

### Innovative System Directions

Authors are encouraged to submit papers on “big integration” that showcase innovations that extend beyond a single IC and into the system level. These include focus areas of **Machine and deep learning, FPGA-based accelerators, Internet of Things, Industrial electronics, Big Data management, Biomedical applications, and Robotics and autonomous transportation.** Focus sessions comprising invited and contributed papers will be offered.

### Best Student Paper Award

The Best Student Paper Award selection will be based on quality and presentation of the paper at the Symposium. The winning student will be presented an award at the 2022 Symposium opening session. **The student must be enrolled as a full-time student at the time of submission, be the leading author and presenter of the paper,** and apply for the award when submitting the paper.

### Secretariat for VLSI Symposia

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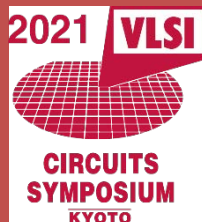
### Joint Circuits and Technology Focus Sessions

The VLSI Symposium on Technology will be held in parallel to Circuit sessions. To synergize on topics of common interest Joint Circuits and Technology focus sessions will be offered in special areas of mutual interest that enable ubiquitous intelligence, smart mobility, and other applications of VLSI, such as:

- **Tech & system for new computing:** design and technology for high performance AI, neuromorphic computing, beyond Von Neumann architecture, etc.
- **Sensor and system for lifestyle transformation:** IoT, security, biometrics, navigation, advanced imaging and LiDAR, radar, human machine interface
- **Si-photonics:** high-speed optical interconnection, optical computing, integrated optical sensors
- **DTCO/STCO/Heterogeneous Integration:** design technology co-optimization, inter-chip communication, SiP architectures and applications, advanced packaging
- **Embedded memory technology and design:** SRAM, DRAM, NOR Flash, PCRAM, ReRAM, MRAM, FeRAM

### Paper Submission

Prospective authors must submit two-page papers and abstracts using the Symposia's website, [www.vlssymposium.org](http://www.vlssymposium.org). Accepted papers will be published as submitted, with **no revisions permitted**. Authors must follow detailed instructions provided within the “Authors” section of the website, including the Authors' Guide and Pre-publication Policy. Extended versions of selected papers from the Symposium will be published in a **Special Issue of the IEEE Journal of Solid-State Circuits and IEEE Solid-State Circuits Letters.**



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